Rapidio optical switch

Overview

The CPS-1848 device utilizes a 5th generation switch fabric, building upon Gen1 CPS and Tsi switching architectures. This new switch uses patent pending features to minimize latency, ensure scheduling. RapidIO is a packet-switched interconnect technology used to link electronic components. RapidIO follows common electrical standards, such as those used in Ethernet, and can connect. A smarter alternative is RapidIO, an open, standards-based interconnection technology for chip- and board-level communications in medium- and large-size embedded systems. This technology enables high-speed, packet-switched, peer-to-peer connectivity between ASICs, DSPs, FPGAs, microprocessors. Designed to provide optimal support for VXS multi board applications – using Abaco Systems' DSP220 VXS quad 8641 processor or alternative multiprocessor systems - the CRX800 offers 18 x4 Serial RapidIO (sRIO) payload ports, four x4 sRIO inter-switch ports and a PowerPC MPC8548 management processor. SmartDV's RapidIO Switch IP is a high-performance interconnect solution compliant with the RapidIO specification up to version 4. The Axiro RapidIO switches are low power, low latency with industry-leading interoperability.

Article Content

Unlocking High-Speed Networks: Why SRIO Optical Switches Are

At its core, an SRIO (Serial RapidIO) optical switch is purpose-built for applications that require deterministic low latency and high bandwidth—such as interconnecting processors in

Serial RapidIO Simple Communication Example for the

The included Serial RapidIO core is configured to run two independent ports (Port 0 and Port 1) at 3.125 Gbps. It uses the Initiator/Target

RADNET 1848-PS; CS-17-A05-08

RADNETTM 1848-PS radiation-hardened serial RapidIO® packet switch ASSP The RADNET 1848-PS application specific standard product (ASSP) provides high-performance serial RapidIO revision 2.1

A Gigabit Fiber Optical Bus Based on the RapidIO Technology

The RapidIO architecture is an interconnect technology based on a high-performance and packet-switched interconnect technology. The paper uses the RapidIO Interconnect technology to

Engineering:RapidIO

History The RapidIO protocol was originally designed by Mercury Computer Systems and Motorola (Freescale) as a replacement for Mercury''s RACEway proprietary bus and Freescale''s PowerPC

80HCPS1848 | RapidIO Switch

The CPS-1848 device utilizes a 5th generation switch fabric, building upon Gen1 CPS and Tsi switching architectures. This new switch uses patent pending

RapidIO Switch IP | SmartDV

SmartDV''s RapidIO Switch IP is a high-performance interconnect solution compliant with the RapidIO specification up to version 4.1. It enables efficient, low-latency packet switching across multiple

RapidIO Switch IP | SmartDV

It enables efficient, low-latency packet switching across multiple endpoints in complex embedded systems, supporting applications in wireless infrastructure, aerospace, defense, and high

CPS-1848 Datasheet (PDF)

Part #: CPS-1848. Download. File Size: 1MbKbytes. Page: 85 Pages. Description: 18-Port, 48-Lane, 240Gbps, Gen2 RapidIO Switch. Manufacturer: Renesas

Buy an audio switch? | Marmitek

An optical switch makes it possible to connect various audio sources, such as a Blu-ray player, game console or streaming device, to one input of your amplifier, soundbar or home cinema system. This

RapidIO® Interconnect for Servers and Supercomputing

RapidIO: the Ideal Scalable Interconnect for Servers and Super Computing Historically, a server was a computer connected to an ethernet network with network interface con-troller. Over time this LAN

CRX800 Serial RapidIO Switch

Designed to provide optimal support for VXS multi board applications – using Abaco Systems'' DSP220 VXS quad 8641 processor or alternative multiprocessor

RF & mmWave Semiconductor Solutions for 5G

The Axiro RapidIO switches are low power, low latency with industry-leading interoperability, configurability, and power per port. Today, Axiro is shipping six

10-Quad RapidIO® Switch

10-Quad RapidIO® Switch 1 Device Overview The CPS-10Q (80KSW0005) is a serial RapidIO switch whose functionality is central to routing packets for distribution among DSPs, processors, FPGAs,

RXS RapidIO Switches Product Brief

The switch configurations include a 24-port 48-lane (RXS2448) switch and a 16-port 32-lane (RXS1632) switch. These RapidIO® products continue the tradition of delivering the lowest latency (~100ns)

CPS-1848 User Manual

RapidIO switching supports standard RapidIO routing functionality, including unicast and up to 40 multicast groups. The CPS-1848 exceeds the RapidIO Specification (Rev. 2.1)to support broadcast

Datasheet

Description The CPS-1616 (80HCPS1616) is a RapidIO Specification (Rev. 2.1) compliant Central Packet Switch whose functionality is central to routing packets for distribution among DSPs,

Serial RapidIO Physical Layer Interface IP User s Guide

RapidIO systems contain endpoint and switch processing elements. The RapidIO interconnect architecture is parti-tioned into a layered hierarchy of specifications which includes Logical, Common

16-Port Serial RapidIO® Switch

The CPS-16, device number IDT80KSW0002, is a serial RapidIO (sRIO) switch whose functionality is central to routing packets for distribution among DSPs, processors, FPGAs, other switches, or any

Implementing RapidIO

RapidIO is particularly attractive because it supports serial connections, which OEMs prefer as a way to reduce cost and increase system bandwidth. RapidIO also cuts costs through its ability to work with

qnx_rapidio_white_paper.PDF

Already, Motorola and IBM have begun to implement RapidIO inter-faces on next-generation processors. Meanwhile, Altera has released a RapidIO solution for program-mable systems-on-a

Contact Us

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